



1.3.2

**Percentage of students undertaking
project work/field work/
internships**

Greater Noida Institute of Technology (Engg. Institute)

**Plot No. 7, Knowledge Park II, Greater Noida
Uttar Pradesh 201310 India**



Department of Electronics & Communication Engineering

List of students undertaking Project work / field Work/ Internships (Data for the latest completed academic year-2021-22)

Program Name: M.Tech. (VLSI Design)

Program Code: 85


Total No. of students: 05

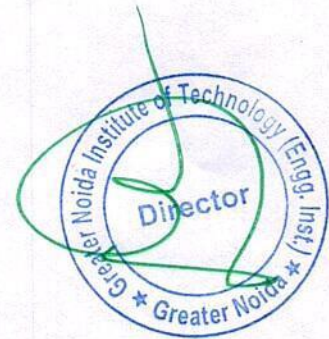
Sl. No.	Student Name	Student Univ. Roll No	Year/Sem	Project/ Internship Title	Project/ Mini Project/ Internship	Place of Work	Duration(In Months or Weeks)
1	Amit Bhandari	2001320085001	2ND YEAR/4TH SEM	IOT based system for accident detection, monitoring and landslide detection in hilly areas and highway light powering system	MAJOR PROJECT	GNIOT, GREATER NOIDA	4 MONTHS
2	Sanjay Singh	2001320085002	2ND YEAR/4TH SEM	Power efficient and energy optimized Full Adder using GNRFFET	MAJOR PROJECT	GNIOT, GREATER NOIDA	4 MONTHS
3	Bhuvaneshwari S	2001320085003	2ND YEAR/4TH SEM	Coherent Low power Full Adder using CNTFET and MGFET in 32 nm Technology	MAJOR PROJECT	GNIOT, GREATER NOIDA	4 MONTHS
4	Farha Naj	2001320085004	2ND YEAR/4TH SEM	Low Power & High speed Dynamic D-Flip Flop based on Gates tied GNRFFET in 16 nm technology length	MAJOR PROJECT	GNIOT, GREATER NOIDA	4 MONTHS
5	Sandeep Kumar	2001320085005	2ND YEAR/4TH SEM	Low power based ternary Half adder using Fin type FET Technology	MAJOR PROJECT	GNIOT, GREATER NOIDA	4 MONTHS



CERTIFICATE

Certified that AMIT BHANDARI (2001320085001) has carried out the research work presented in this thesis entitled "IOT BASED SYSTEM FOR ACCIDENT DETECTION, MONITORING AND LANDSLIDE DETECTION IN HILLY AREAS" and HIGHWAY LIGHT POWERING SYSTEM USING WIND AND MONITORING SYSTEM USING IOT for the award of Master of Technology from Dr. APJ Abdul Kalam Technical University, Lucknow under my/our (print only that is applicable) supervision. The thesis embodies results of original work, and studies are carried out by the student himself/herself (print only that is applicable) and the contents of the thesis do not form the basis for the award of any other degree to the candidate or to anybody else from this or any other University/Institution.


Signature:
Dr. Mukesh Kumar Ojha
HOD, ECE Deptt.
GNIOT, Gr. Noida, UP, INDIA



Date: 1/11/22

CERTIFICATE

This is to certify that the thesis entitled "Power Efficient and Energy Optimized Full Adder Using GNRFPET" submitted by Sanjay Singh, Department of Electronics and Communication Engineering, Greater Noida Institute of Technology, Greater Noida affiliated to Dr. A.P.J. Abdul Kalam Technical University, Lucknow, for the award of the degree of *Master of Technology in VLSI Design*, is a record of bonafide work carried out by them under my supervision, as per AKTU code of academic and research ethics.

The contents of this report have not been submitted and will not be submitted either in part or in full, for the award of any other degree or diploma in this Institute or any other Institute or University. The thesis fulfills the requirements and regulations of the University and in my opinion meets the necessary standards for submission.

Place : Greater Noida

Date : 1/11/2022



Pooja Singh
1/11/2022
Signature of the Guide

The thesis is satisfactory / unsatisfactory

Internal Examiner

External Examiner

Approved by


Head of the Department

CERTIFICATE

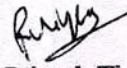
Certified that Bhuvaneshwari S (2001320085003) has carried out the research work presented in this thesis entitled "Coherent Low Power Full Adders Using CNTFET And MGFET in 32nm Technology" for the award of Master of Technology from Dr APJ Abdul Kalam technical University, Lucknow under Our Supervision. The thesis embodies results of original work, and studies are carried out by the student herself and the contents of the theses do not form the basis for the award of any other degree to the candidate or to anybody else from this or any other University/Institution.

Signature



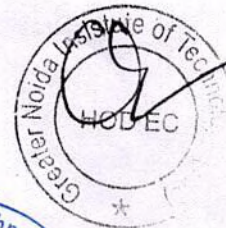
Mr Shiv Narain Gupta
Assistant Professor- ECE Dept
GNIOT

Signature



Mr. Priyesh Tiwari
Assistant Professor-ECE Dept
GNIOT

Date: 1/11/22



CERTIFICATE

Certified that Farha Naj (Enrollment No: 2001320085004) has carried out the research work presented in this thesis entitled "LOW POWER AND HIGH-SPEED DYNAMIC D-FLIP FLOP BASED ON GATES TIED GNR FET IN 16nm TECHNOLOGY LENGTH" for the award of Master of Technology from Dr. APJ Abdul Kalam Technical University, Lucknow under my supervision. The thesis embodies results of original work, and studies are carried out by the student herself and the content of the thesis do not form the basis for the award of any other degree to the candidate or to anybody else from this or any other University/Institution.

Pooja

Signature

(Name of Supervisor)

Dr Pooja Saxena

(Designation)

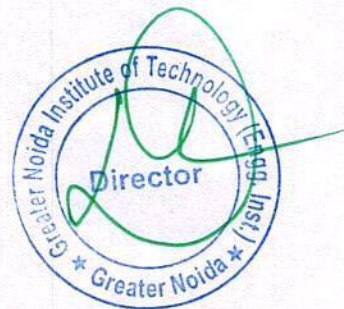
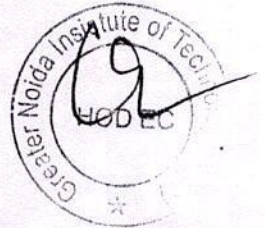
Assitant Professor

(Address)

Greater Noida

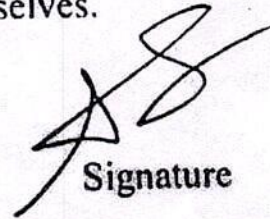
Date:

4/11/2022



CERTIFICATE

It is acknowledged that Sandeep Kumar (091503143970) worked under my direction to complete the research for this thesis, "Low Power Based Ternary Half Adder Using Fin Type Field Effect Transistor Technology," which is required for the award of a Master of Technology from Dr. APJ Abdul Kalam Technical University, Lucknow. The candidate will not be granted any other degree from this or any other University/Institution based on the contents of the thesis, which represents the results of original work and studies completed by the student themselves.



Signature

Dr. Anil Kumar Dubey

Associate Professor

GNIOT Greater Noida

